

PATENT APPLICATION

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9-13-03



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Confirmation No: 4931

In re the Application of:

Art Unit: 2819

Yasuoru MATSUZAKI

Examiner: Vibol Tan

Serial No.: 10/003,048

Docket No.: 108397-00052

Filed: December 6, 2001

For: INPUT/OUTPUT INTERFACE AND SEMICONDUCTOR INTEGRATED  
CIRCUIT HAVING INPUT/OUTPUT INTERFACE

RESPONSE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 8, 2003

Sir:

In response to the outstanding Office Action dated June 11, 2003, (hereinafter "outstanding Action") with the response time falling within the shortened statutory period, please reconsider the above-identified application in view of the following remarks:

RECEIVED  
SEP - 9 2003  
TECHNOLOGY CENTER 2800